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PATENT
Conf. No. 5260

IN THE UNITED STATES PATENT OFFICE

Applicant: Andy T. Nguyen
Assignee: Xilinx, Inc.
Title: Counter-Based Phased Clock Generator Circuits and Methods
Serial No.: 10/696,226 File Date: 10/28/2003
Examiner: Minh T. Nguyen Art Unit: 2816
Docket No.: X-1464-1P-1P US Conf. No.: 5260

Mail Stop AMENDMENT
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the Office Action mailed October 18, 2004, Applicant submits the following amendments and remarks.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.